

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

: EXAMINER: GEBRESILASSIE, K. K.

IN RE APPLICATION OF

SANAE ITO, ET AL.

SERIAL NO: 10/028,319

RCE FILED: HEREWITH : GROUP ART UNIT: 2128

FOR: SIMULATION METHOD, SIMULATION PROGRAM, AND SEMICONDUCTOR DEVICE MANUFACTURING METHOD EACH EMPLOYING BOUNDARY CONDITIONS

AMENDMENT UNDER 37 CFR §1.114

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 4, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.